



DOCKET NO: 219030US2S

"RESPONSE UNDER 37 CFR 1.116-
EXPEDITED PROCEDURE EXAMINING
GROUP 2814"

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
TOSHITAKE YAEGASHI : EXAMINER: PIZARRO-CRESPO, M. D.
SERIAL NO: 10/058,343 :
FILED: JANUARY 30, 2002 : GROUP ART UNIT: 2814
FOR: NON-VOLATILE :
SEMICONDUCTOR MEMORY DEVICE
IN WHICH SELECTION GATE
TRANSISTORS AND MEMORY CELLS
HAVE DIFFERENT STRUCTURES

AMENDMENT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated March 11, 2004, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Amendments to the Drawings begin on page 16 of this paper and include five attached replacement sheets.

Remarks/Arguments begin on page 17 of this paper.